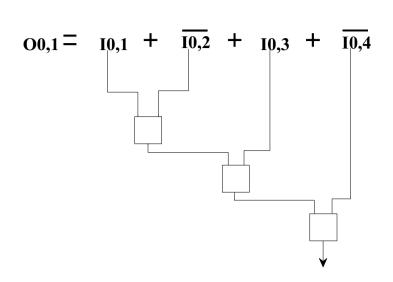
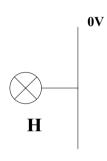


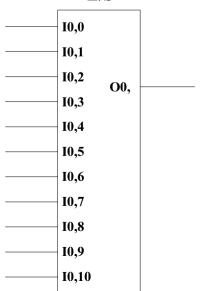
# **ELECTRIQUE**



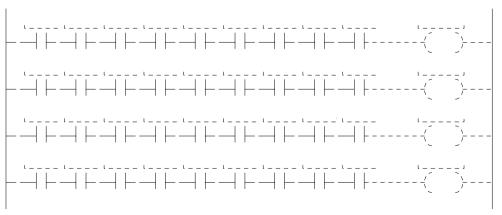




E/S

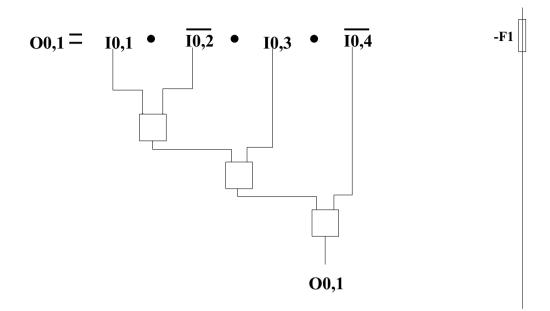


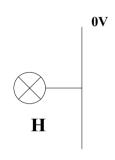




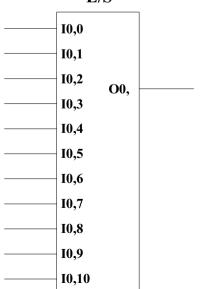


# **ELECTRIQUE**

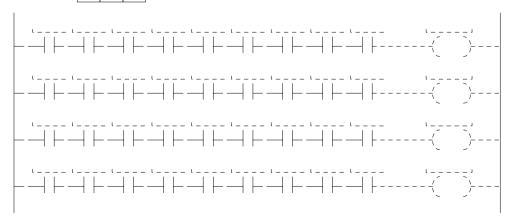




E/S

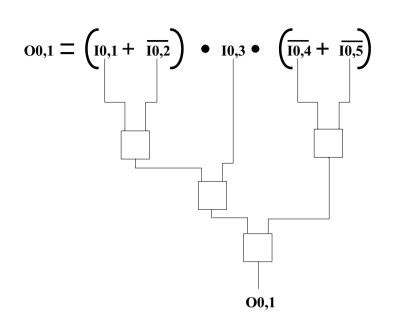


LABEL:

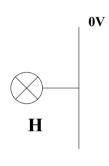




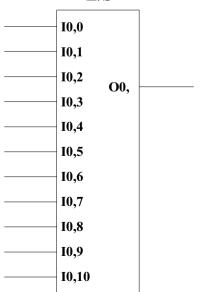
# **ELECTRIQUE**



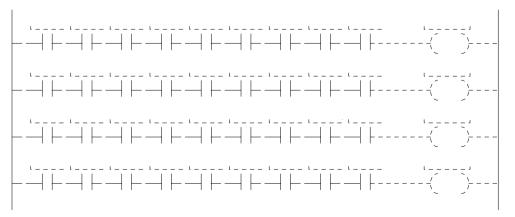




E/S

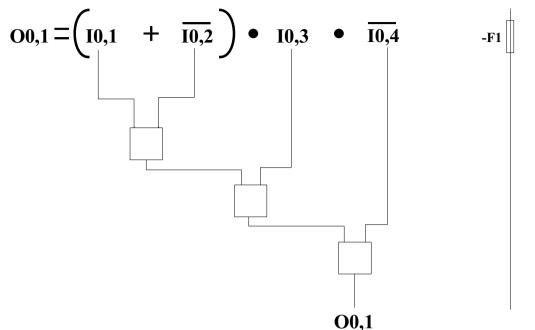


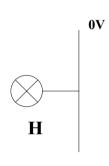




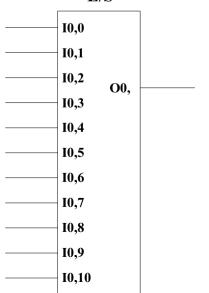




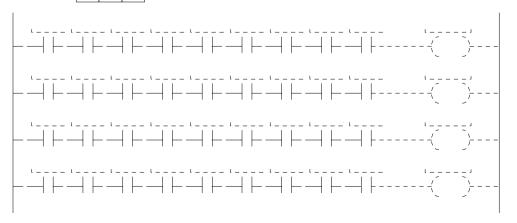




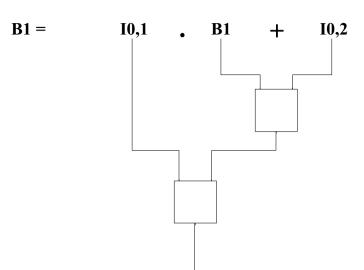
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LABEL:

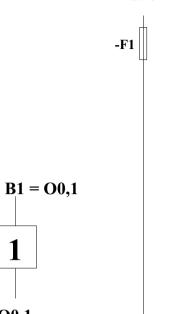






**B1** 

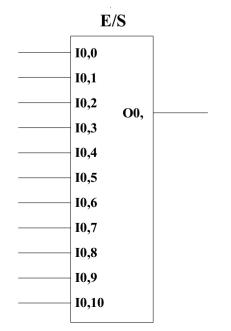
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# **ELECTRIQUE**

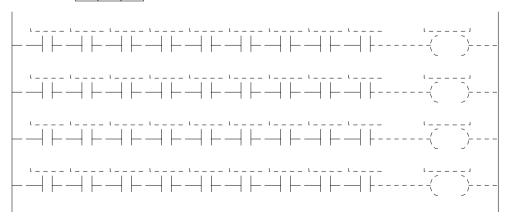


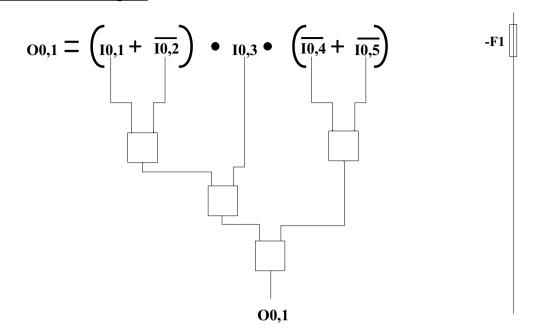
## **AFFECTATION DES ENTREES SORTIES**

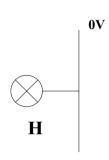


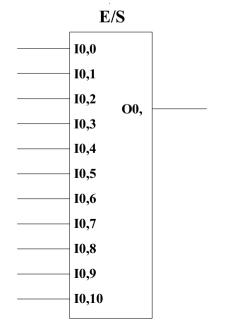


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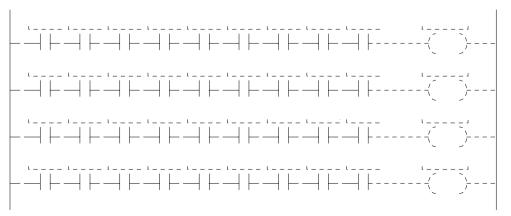






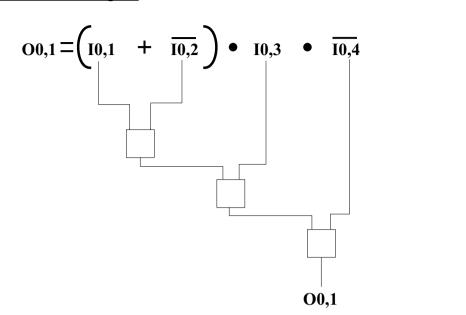


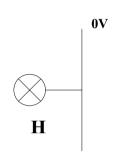




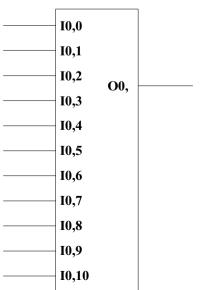


# **ELECTRIQUE**

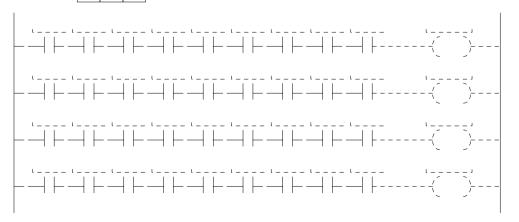






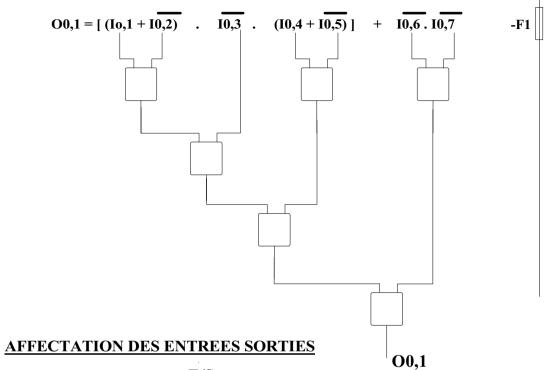


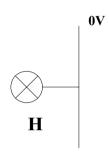




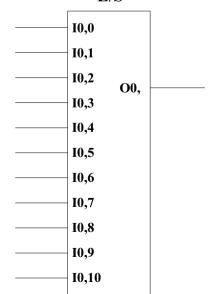


# **ELECTRIQUE**





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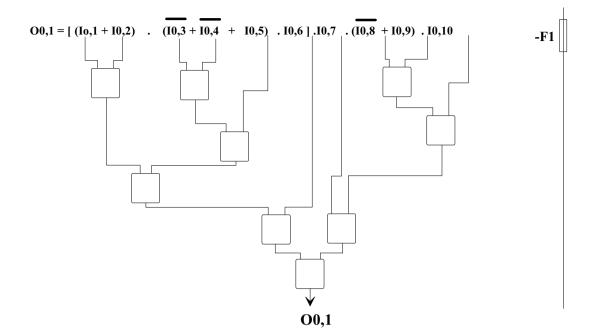


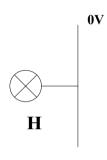
LABEL:

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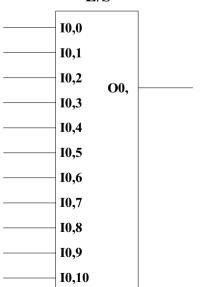


# **ELECTRIQUE**

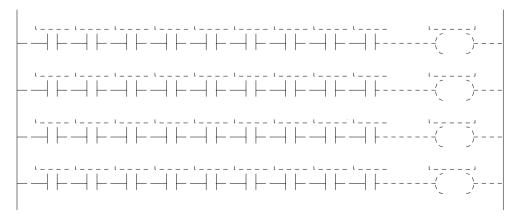




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LABEL:

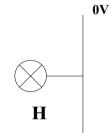


# **SCHEMA LOGIQUE**

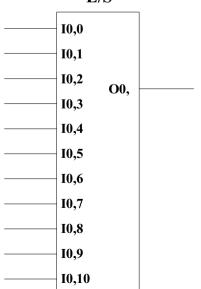


## **ELECTRIQUE**





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LABEL:

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